

**Application Data Sheet**

**Application Information**

Application number::	Not Yet Assigned
Application Type::	Regular
Subject Matter::	Utility
Suggested Group Art Unit::	N/A
CD-ROM or CD-R?::	None
Sequence submission?::	None
Computer Readable Form (CRF)?::	No
Title::	STRUCTURE AND METHOD OF MULTIPLEXING BITLINE SIGNALS WITHIN A MEMORY ARRAY
Attorney Docket Number::	INTECH 3.0-090
Request for Early Publication?::	No
Request for Non-Publication?::	No
Suggested Drawing Figure::	3B
Total Drawing Sheets::	7
Small Entity?::	No
Petition included?::	No
Secrecy Order in Parent Appl.?::	No

**Applicant Information**

Applicant Authority Type::	Inventor
Primary Citizenship Country::	Austria
Status::	Full Capacity
Given Name::	Peter
Family Name::	Poechmueller
City of Residence::	Unterhaching
Country of Residence::	Germany
Street of mailing address::	Adejeweg 7
City of mailing address::	Unterhaching
Postal or Zip Code of mailing address::	D-82008

**Correspondence Information**

Correspondence Customer Number:: 000530

**Representative Information**

Representative Customer Number:: 000530

**Assignee Information**

Assignee name:: Infineon Technologies North America Corp.  
Street of mailing address:: 1730 North First Street  
City of mailing address:: San Jose  
State or Province of mailing address:: CA  
Postal or Zip Code of mailing address:: 95112